
16 Bit Embedded Controller Handbook 1991

1-bit ecc, x8 and x16 i/o, 1.8 v v slc nand flash for embedded - document number: 002-00330 rev. *1 page 3 of 71 s34ms01g1 s34ms02g1 s34ms04g1 1. general description the cypress s34ms01g1, s34ms02g1, and s34ms04g1 series is offered in 1.8 vcc and vccq power supply, and with x8 or x16 i/o interface.

embedded graphics on stm32f4 - emcu - ltdc main features - (1/3) •24-bit rgb parallel pixel output; 8 bits-per-pixel (rgb888) •ahb 32-bit master with burst access of 16 words to any system **datasheet - stm8s003f3 stm8s003k3 - value line, 16-mhz ...** - this is information on a product in full production. august 2018 ds7147 rev 10 1/103 stm8s003f3 stm8s003k3 value line, 16-mhz stm8s 8-bit mcu, 8-kbyte flash memory, **freescale semiconductor data sheet: advance information** - introduction mpc5604b/c microcontroller data sheet, rev. 13 freescale semiconductor 3 1 introduction 1.1 document overview this document describes the features of the family and options available within the family members, and highlights important electrical and physical **dlpc900 digital controller for advanced light control ...** - osc voltage supplies fan pclk, de hsync, vsync 24-bit rgb data i2c i2c digital video usb vrst, vbias, voff dmd ctl, data red, green, blue pwm led strobes dlp6500 **mobile communications - o2micro** - 3 mobile communications product guide part number description package(s) available (lead-free) page no. power management ics oz8070a usb protection ic and battery charger 20 qfn 7 oz8618 battery charge controller with power selector 24 qfn 8 oz8681 smbus level 2 battery charger 16 qfn 9 oz8682 smbus level 2 battery charger with hybrid power boost **arm®-based cortex®-m7 32b mcu+fpu, 462dmips, up to 1mb ...** - this is information on a product in full production. february 2016 docid027590 rev 4 1/227 stm32f745xx stm32f746xx arm®-based cortex®-m7 32b mcu+fpu, 462dmips, up to 1mb flash/320+16+ 4kb ram, usb otg hs/fs, ethernet, 18 tims, 3 adcs, 25 com itf, cam & lcd **introduction to the controller area network (can) (rev. b)** - 0!8bytes data c r c a c k e o f e o f f i s i i d e r r00 d l c 11-bit identifier r t r s o f t i standard can or extended can sloa101b-august 2002-revised may 2016 3 submit documentation feedback **atmel | smart sam g55g sam g55j datasheet** - atmel-11289f-atarm-sam-g55g-sam-g55j-datasheet_27-may-16 description the atmel® | smart sam g55 is a series of flash microcontrollers based on the high-performance 32-bit arm® cortex®-m4 risc processor with fpu (floating point unit). it operates at a maximum speed of 120 mhz and features 512 kbytes **1. introduction to embedded system design** - 1 - 15 swiss federal institute of technology computer engineering and networks laboratory characteristics of embedded systems (3) many es must meet real-time constraints:a real-time system must react to stimuli from the controlled object (or the operator) within the time interval dictated by the **usb 2.0 hi-speed hub controller - microchip technology** - usb251xb/xbi ds00001692c-page 2 2010 - 2015 microchip technology inc. to our valued customers it is our intention to provide our valued customers with the bes t documentation possible to ensure successful use of your micro chip **i 6 series of applications processors** - scalable multicore solutions breaking the boundaries of user experience i 6 series of applications processors the i 6 series of applications processors is a feature- and performance-scalable **bosch - embedded system design: a unified hardware ...** - bosch can specification version 2.0 1991, robert bosch gmbh, postfach 50, d-7000 stuttgart 1 thi d t t d ith f m k 4 0 4 **i210 i211 datasheet - intel** - introduction—ethernet controller i210 5 1.0 introduction the intel® ethernet controller i210 (i210) is a single port, compact, low power component that supports gbe designs. the i210 offers a fully-integrated gbe media access control (mac), physical **cisco ucs c220 m5 sff rack server spec sheet** - cisco systems publication history 170 west tasman dr san jose, ca, 95134 rev b.20 april 10, 2019 cisco spec sheet cisco ucs c220 m5 rack server (small form factor disk drive model) **s29gl01gs/s29gl512s s29gl256s/s29gl128s 1-gbit (128 mbyte ...** - document number: 001-98285 rev. *r page 3 of 108 s29gl01gs/s29gl512s s29gl256s/s29gl128s performance summary maximum read access times density voltage range **2080-sg001e-en-p micro800 programmable controllers ...** - 4 publication 2080-sg001e-en-p - march 2019 chapter 1 select a micro800 controller micro830® controllers are designed for standalone machine control applications. they have flexible communications and i/o capabilities with up to five plug-ins. **w5100 datasheet v1 1 6 - sparkfun electronics** - ir (interrupt register) [r] [0x0015] [0x00] **hardware reference guide - hp** - 1 product overview standard features the hp rp9 g1 retail system includes the following features. integrated all-in-one (aio) form factor designed for long-term deployment within general retail, hospitality, and other markets **future technology devices international ltd ft230x - ftdi** - document no.: ft_000566 clearance no.: ftdi# 260 mtp **xa spartan-6 automotive fpga family overview - xilinx** - xa spartan-6 automotive fpga family overview ds170 (v1.3) december 13, 2012 xilinx product specification 3 configuration xa spartan-6 fpgas store the customized configuration data in sram-type internal latches. **cyclone v device overview - intel** - cyclone v device overview the cyclone ® v devices are designed to simultaneously accommodate the shrinking power consumption, cost, and time-to-market requirements; and the increasing bandwidth requirements for high-volume and cost-sensitive applications. **future technology devices international ltd ft231x - ftdi** - future technology devices international ltd ft231x - ftdi ... x) **ar0130cs - 1/3-inch cmos digital image sensor** - ar0130cs onsemi 4 figure 2. typical configuration: parallel pixel data interface vdd_io vdd vdd_pll vaa vaa_pix from controller master clock (6 – 50 mhz) 1.5 k aa 2 1.5 k aa

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